

CLAIM AMENDMENT

Claim 24 has been amended, and claims 22, 23, 32 and 33 have been canceled.

Claim 1-23(Canceled).

Claim 24 (currently amended): A method of forming a semiconductor chip, comprising:

providing a substrate having a main surface, the main surface including a frame-shaped first area, which is along sides of the main surface, and a second area encompassed by the first area;

forming a pad in the first area;

forming a bump electrode on the pad; [and]

forming a first barrier metal formed under the bump electrode,

forming at least one supporting member in the second area, wherein forming the supporting member and the bump electrode includes forming the supporting member and the bump electrode of the same material, and

forming a second barrier metal formed under the supporting member,

the first and second barrier metals being formed of the same material, and the first and second barrier metals having the same thickness.

Claim 25 (original): A method of forming a semiconductor chip as claimed in claim 24, wherein forming the pad includes forming the pad to have a second thickness, forming the bump electrode includes forming the bump electrode to have a third thickness and forming the support member includes forming the support member to have a first thickness, which is substantially equals to the sum of the second and third thicknesses.

Claim 26 (original): A method of forming a semiconductor chip as claimed in claim 24, wherein forming the pad includes forming the pad to have a second thickness, forming the bump electrode includes forming the bump electrode to have a third thickness and forming the support member includes forming the support member to have a first thickness, which is less than the sum of the second and third thicknesses.

Claim 27 (original): A method of forming a semiconductor chip as

claimed in claim 24, further comprising, forming a resin connector on the support member.

Claim 28 (previously presented): A method of forming a semiconductor package, comprising:

providing a substrate having a main surface, the main surface including a frame-shaped first area, which is along sides of the main surface, and a second area encompassed by the first area;

forming a pad in the first area;

forming a bump electrode on the pad;

forming at least one supporting member on the second area, the support member having a tip;

providing a tape substrate having a land electrode;

connecting the bump electrode to the land electrode; and

connecting the tip of the supporting member to the tape substrate.

Claim 29 (previously presented): A method of forming a semiconductor package as claimed in claim 28, wherein connecting the tip of the supporting

member to the tape substrate includes connecting the tip of the supporting member to the tape substrate by a connector resin.

Claim 30 (original): A method of forming a semiconductor package as claimed in claim 28, wherein the tape substrate further includes a dummy land electrode, and connecting the supporting member to the tape substrate includes connecting the supporting member to the tape substrate by connecting the tape substrate to the dummy land electrode.

Claim 31 (previously presented): A method of forming a semiconductor package, comprising:

providing a semiconductor chip including a main substrate having a main surface, the main surface including a frame-shaped first area, which is along sides of the main surface, and a second area encompassed by the first area, a pad formed in the first area, a bump electrode formed on the pad, and at least one supporting member formed on the second area;

preparing an assemble apparatus having a recess at its center;

providing a tape substrate having a land electrode;

placing the tape substrate on the assemble apparatus wherein an area on the tape substrate, which is encompassed by the land electrode, is disposed on the recess of the assemble apparatus; and

mounting the semiconductor chip on the tape substrate, and connecting the bump electrode to the land electrode.

Claims 32 –33 (Cancelled).

Claim 34 (previously presented): A method of forming a semiconductor package as claimed in claim 30, further comprising:

forming a solder ball on the dummy land electrode,

wherein the supporting member is connected to the tape substrate by the solder ball.

Claim 35 (previously presented): method of forming a semiconductor package as claimed in claim 30, wherein the dummy land electrode is arranged under the supporting member.